

200300117-1

DEC 21 2004

Confirmation No. 9140

## In the United States Patent and Trademark Office

## FAX COVER SHEET

In re Patent Application of:	)	
Adam L. Ghozeil et al.	)	Date: December 21, 2004
	)	
Serial No. 10/620,858	)	Group Art Unit: 2814
Filed: 7/15/2003	)	Examiner: Nguyen, Dilinh
Confirmation No. 9140	)	
	)	
Attorney Docket No. 200300117-1	)	
	)	
For: ARRAY OF NANOSCOPIC	)	
MOSFET TRANSISTORS	)	
AND FABRICATION METHODS	)	

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Received this date by the United States Patent and Trademark Office:

## RESPONSE / AMENDMENT

Consisting of this cover sheet (one sheet), Transmittal Letter with fax certification and fee authorization (one sheet in duplicate), and Amendment (7 sheets), a total of 10 sheets in this transmission.

Receipt directed to:  
Hewlett-Packard Company  
Attn: Chris Griffin  
Legal Department – Mailstop 314  
11307 Chinden Blvd.  
Boise, ID 83714-1021  
Fax: (208) 396-3958

HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P. O. Box 272400  
Fort Collins, Colorado 80527-2400

## PATENT APPLICATION

ATTORNEY DOCKET NO. 200300117-1

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Adam L. Ghozeil et al.

Confirmation No.: 9140

Application No.: 10/620,858

Examiner: Nguyen, Dilinh P.

Filing Date: 07/15/2003

Group Art Unit: 2814

Title: ARRAY OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS

Mail Stop Amendment  
Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment ( ) Petition to extend time to respond  
( ) New fee as calculated below ( ) Supplemental Declaration  
(X) No additional fee  
( ) Other: \_\_\_\_\_ (fee \$ \_\_\_\_\_)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS		MINUS		= 0	X \$50	\$ 0
INDEP. CLAIMS		MINUS		= 0	X \$200	\$ 0
( ) FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$360	\$ 0
EXTENSION FEE	1ST MONTH \$120.00	2ND MONTH \$450.00	3RD MONTH \$1020.00	4TH MONTH \$1590.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Adam L. Ghozeil et al.

By Theodore R. Touw

Theodore R. Touw

Attorney/Agent for Applicant(s)

Reg. No. 36,702

Date: 12-21-2004

HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P. O. Box 272400  
Fort Collins, Colorado 80527-2400

## PATENT APPLICATION

ATTORNEY DOCKET NO. 200300117-1

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Adam L. Ghozeil et al.

Confirmation No.: 9140

Application No.: 10/620,858

Examiner: Nguyen, Dilinh P.

Filing Date: 07/15/2003

Group Art Unit: 2814

Title: ARRAY OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS

Mail Stop Amendment  
Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

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Respectfully submitted,

Adam L. Ghozeil et al.

By Theodore R. Touw

Theodore R. Touw

Attorney/Agent for Applicant(s)  
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Date: 12-21-2004

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Rev 12/04 (TnAndFax)

- Attach as First Page to Transmitted Papers -

(X) I hereby certify that this paper is being transmitted  
to the Patent and Trademark Office facsimile  
number (703) 872-9306 on 12-21-2004  
Number of pages:

Typed Name: Theodore R. Touw

Signature: Theodore R. Touw

Attorney Docket No. 200300117-1

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Confirmation No. 9140

DEC 21 2004

In re Patent Application of:  
Adam L. Khozeil et al.

Date: December 18, 2004

Serial No. 10/620,858  
Confirmation No. 9140  
Filed 07/15/2003

Group Art Unit: 2814

Examiner: Dilinh P. Nguyen

For: ARRAY OF NANOSCOPIC  
MOSFET TRANSISTORS AND  
FABRICATION METHODS**AMENDMENT**Hon. Commissioner of Patents and Trademarks  
Alexandria, VA 22313

Dear Sir:

In response to the communication from the Examiner dated Nov. 3, 2004, please amend the application identified above as follows:

In the claims:

Claim 1, line 10, cancel the word "optionally".

Claim 3, line 2, replace the phrase "source and drain regions" with  
-first doped regions-.Claim 8, line 3, replace "second source and drain regions" with  
-second doped regions-.

These amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.